

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al. Art Unit : 2811
Serial No. : 09/814,255 Examiner : Ori Nadav
Filed : March 21, 2001 Conf. No. : 5578
Title : SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT WITH
CERTIFICATION UNDER 37 CFR 1.97(E)**

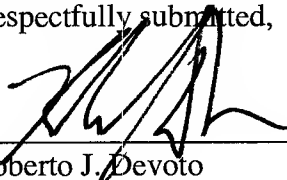
Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. I, the undersigned, hereby certify that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application, the communication being dated December 25, 2007, and that this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

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Respectfully submitted,

Date: 1/19/08



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